



N-Channel 30-V (D-S) MOSFET with Schottky Diode

MOSFET PRODUCT SUMMARY			
V_{DS} (V)	$r_{DS(on)}$ (Ω)	I_D (A)	Q_g (Typ)
30	0.035 at $V_{GS} = 10$ V	7.4	4.2 nC
	0.052 at $V_{GS} = 4.5$ V	6.1	

SCHOTTKY PRODUCT SUMMARY		
V_{KA} (V)	V_F (V) Diode Forward Voltage	I_F (A) ^a
30	0.470 at 3 A	3

FEATURES

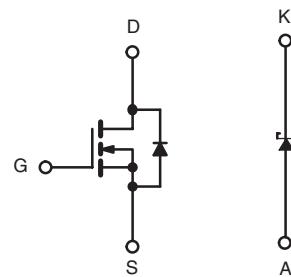
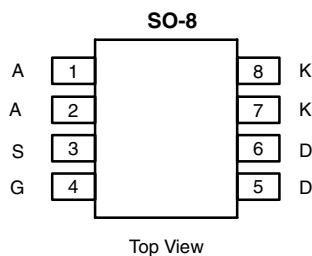
- LITTLE FOOT® Plus Power MOSFET



RoHS
COMPLIANT

APPLICATIONS

- Load Switch for Portable Applications
 - Ideal for Boost Circuits
- HDD Driver



N-Channel MOSFET

Ordering Information: Si4620DY-T1-E3 (Lead (Pb)-free)

ABSOLUTE MAXIMUM RATINGS $T_A = 25$ °C, unless otherwise noted			
Parameter	Symbol	Limit	Unit
Drain-Source Voltage (MOSFET)	V_{DS}	30	V
Reverse Voltage (Schottky)	V_{KA}	30	
Gate-Source Voltage (MOSFET)	V_{GS}	± 20	
Continuous Drain Current ($T_J = 150$ °C) (MOSFET)	I_D	7.5	
$T_C = 25$ °C		6	
$T_C = 70$ °C		6	
$T_A = 70$ °C		4.8	
Pulsed Drain Current (MOSFET)	I_{DM}	40	A
Continuous Source Current (MOSFET Diode Conduction)	I_S	2.6	
$T_C = 25$ °C		1.7 ^{a, b}	
Average Forward Current (Schottky)	I_F	3	
Pulsed Forward Current (Schottky)	I_{FM}	8	W
Maximum Power Dissipation (MOSFET)	P_D	3.1	
$T_C = 25$ °C		2	
$T_C = 70$ °C		2 ^{a, b}	
$T_A = 25$ °C		1.3 ^{a, b}	
Maximum Power Dissipation (Schottky)	P_D	3	
$T_C = 25$ °C		1.9	
$T_C = 70$ °C		1.8	
$T_A = 25$ °C		1.1	
Operating Junction and Storage Temperature Range	T_J, T_{stg}	- 55 to 150	°C
Soldering Recommendations (Peak Temperature)		260	

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THERMAL RESISTANCE RATINGS

Parameter	Symbol	Typical	Maximum	Unit
Maximum Junction-to-Ambient (MOSFET) ^{a, c}	R _{thJA}	53	62.5	°C/W
Maximum Junction-to-Foot (Drain) (MOSFET)	R _{thJF}	30	40	
Maximum Junction-to-Ambient (Schottky)	R _{thJA}	55	65	
Maximum Junction-to-Foot (Drain) (Schottky)	R _{thJF}	32	42	

Notes:

- a. Surface Mounted on FR4 Board.
- b. t ≤ 10 sec.
- c. Maximum under Steady State conditions for MOSFETS is 110 °C/W.
- d. Maximum under Steady State conditions for Schottky is 115 °C/W.

SPECIFICATIONS $T_J = 25 \text{ } ^\circ\text{C}$, unless otherwise noted

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Static						
Drain-Source Breakdown Voltage	V _{DS}	V _{GS} = 0 V, I _D = 250 μA	30			V
V _{DS} Temperature Coefficient	ΔV _{DS/TJ}	I _D = 250 μA		32.5		mV/°C
V _{GS(th)} Temperature Coefficient	ΔV _{GS(th)/TJ}			- 5.3		
Gate-Source Threshold Voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D = 250 μA	1.2		2.5	V
Gate-Source Leakage	I _{GSS}	V _{DS} = 0 V, V _{GS} = ± 20 V			± 100	nS
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = 30 V, V _{GS} = 0 V			1	μA
		V _{DS} = 30 V, V _{GS} = 0 V, T _J = 55 °C			10	
On-State Drain Current ^a	I _{D(on)}	V _{DS} ≤ 5 V, V _{GS} = 4.5 V	40			A
Drain-Source On-State Resistance ^a	r _{DS(on)}	V _{GS} = 10 V, I _D = 6 A		0.028	0.035	Ω
		V _{GS} = 4.5 V, I _D = 4.9 A		0.041	0.052	
Forward Transconductance ^a	g _{fs}	V _{DS} = 15 V, I _D = 6 A		12		S
Dynamic^b						
Input Capacitance	C _{iss}	V _{DS} = 15 V, V _{GS} = 0 V, f = 1 MHz		520	1040	pF
Output Capacitance	C _{oss}			115	230	
Reverse Transfer Capacitance	C _{rss}			55	110	
Total Gate Charge	Q _g	V _{DS} = 15 V, V _{GS} = 10 V, I _D = 6 A		8.6	13	nC
				4.2	6.5	
Gate-Source Charge	Q _{gs}	V _{DS} = 15 V, V _{GS} = 4.5 V, I _D = 6 A		1.8		
Gate-Drain Charge	Q _{gd}			1.5		
Gate Resistance	R _g	f = 1 MHz		2.8		Ω
Turn-On Delay Time	t _{d(on)}	V _{DD} = 15 V, R _L = 3.1 Ω I _D ≈ 4.8 A, V _{GEN} = 4.5 V, R _g = 6 Ω		16	30	ns
Rise Time	t _r			36	54	
Turn-Off Delay Time	t _{d(off)}			21	40	
Fall Time	t _f			17	40	



New Product

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SPECIFICATIONS $T_J = 25^\circ\text{C}$, unless otherwise noted

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Drain-Source Body Diode Characteristics						
Continuous Source-Drain Diode Current	I_S	$T_C = 25^\circ\text{C}$			2.6	A
Pulse Diode Forward Current	I_{SM}				40	
Body Diode Voltage	V_{SD}	$I_S = 1.7 \text{ A}, V_{GS} = 0 \text{ V}$		0.8	1.2	V
Body Diode Reverse Recovery Time	t_{rr}	$I_F = 1.7 \text{ A}, di/dt = 100 \text{ A}/\mu\text{s}, T_J = 25^\circ\text{C}$		20	40	ns
Body Diode Reverse Recovery Charge	Q_{rr}			14	30	nC
Reverse Recovery Fall Time	t_a			14		ns
Reverse Recovery Rise Time	t_b			6		

Notes:

- Pulse test; pulse width $\leq 300 \mu\text{s}$, duty cycle $\leq 2\%$.
- Guaranteed by design, not subject to production testing.

SCHOTTKY SPECIFICATIONS $T_J = 25^\circ\text{C}$, unless otherwise noted

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Forward Voltage Drop	V_F	$I_F = 3 \text{ A}$		0.39	0.470	V
		$I_F = 3 \text{ A}, T_J = 125^\circ\text{C}$		0.35	0.420	
Maximum Reverse Leakage Current	I_{rm}	$V_r = 5 \text{ V}$		0.1	0.2	mA
		$V_r = 5 \text{ V}, T_J = 85^\circ\text{C}$		3.5	17.5	
		$V_r = 5 \text{ V}, T_J = 106^\circ\text{C}$		12	60	
		$V_r = 30 \text{ V}$		0.22	0.5	
		$V_r = 30 \text{ V}, T_J = 85^\circ\text{C}$		10	50	
		$V_r = 30 \text{ V}, T_J = 125^\circ\text{C}$		40	200	
Junction Capacitance	C_T	$V_r = 15 \text{ V}$		100		pF

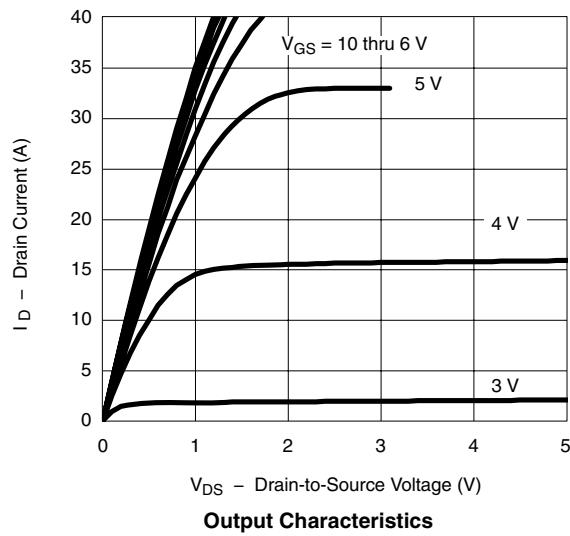
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

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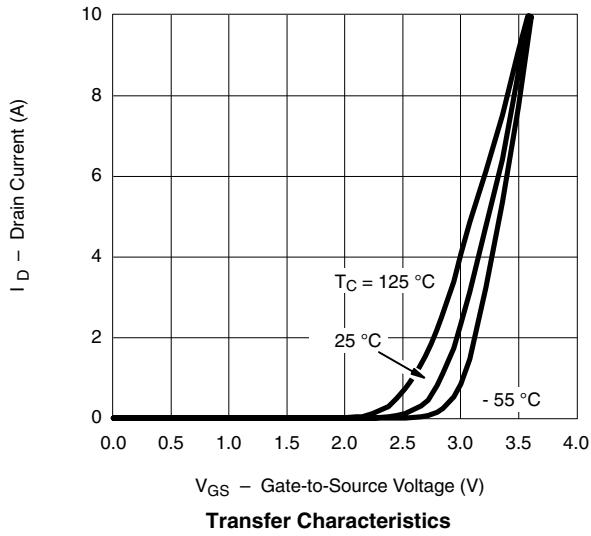
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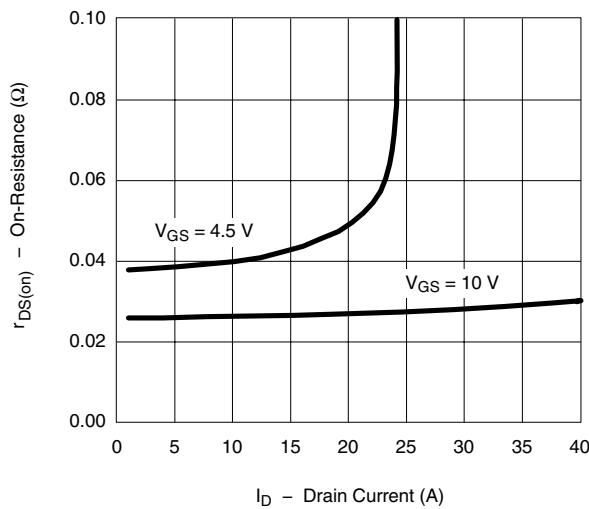
TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



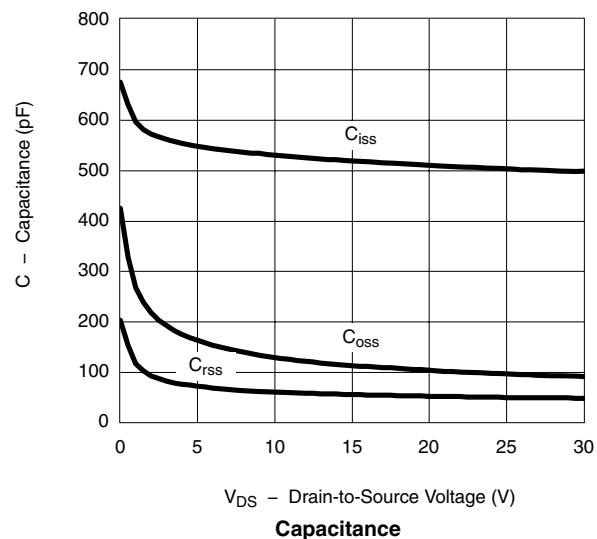
Output Characteristics



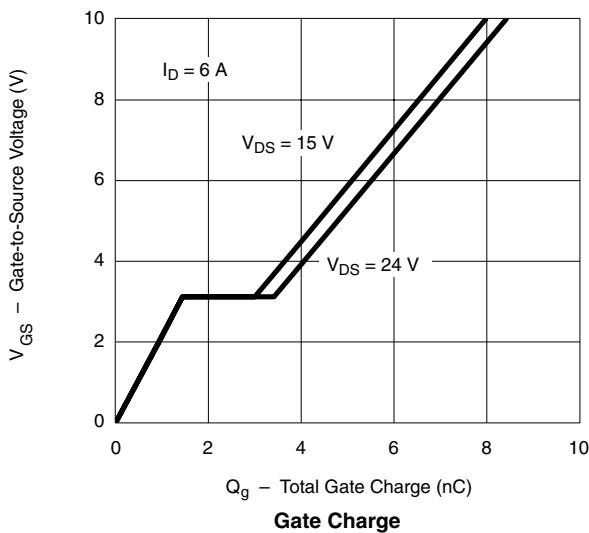
Transfer Characteristics



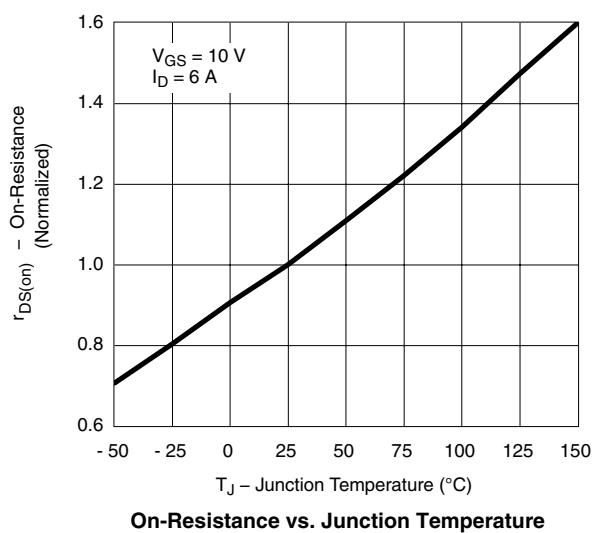
On-Resistance vs. Drain Current and Gate Voltage



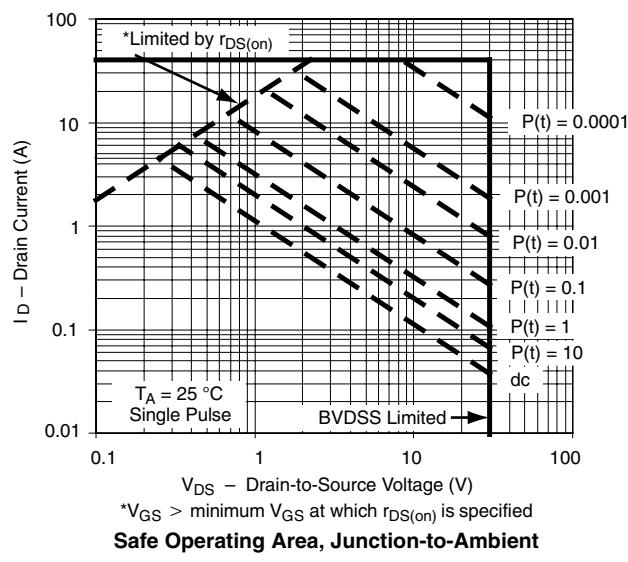
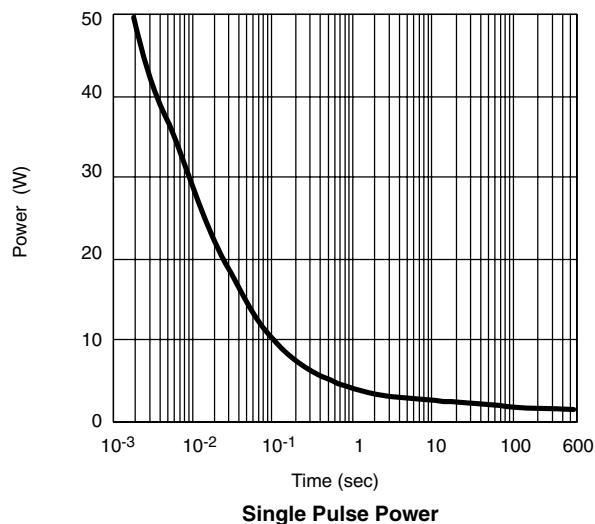
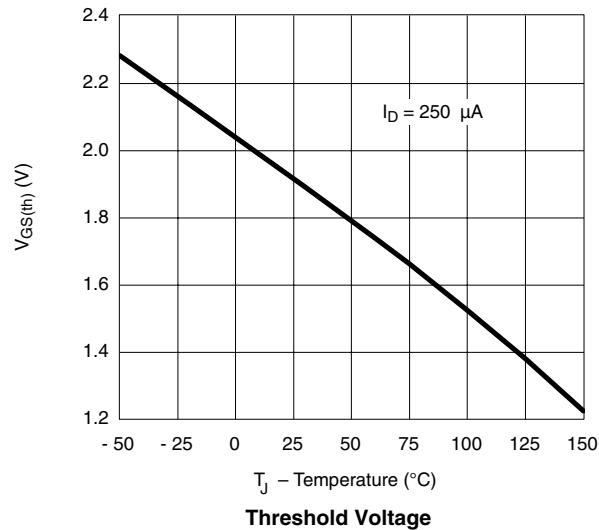
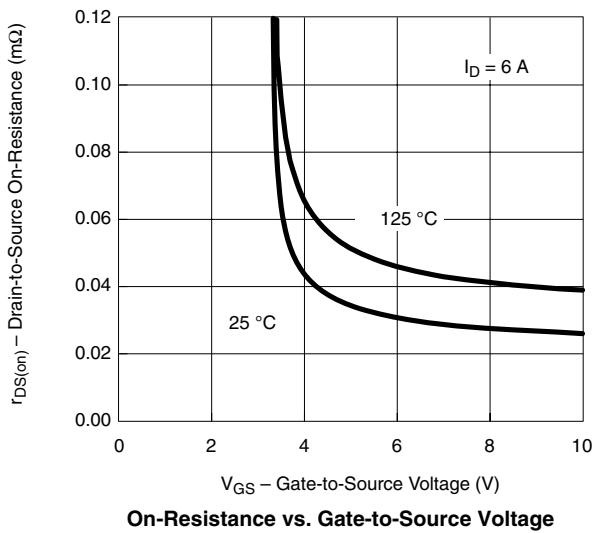
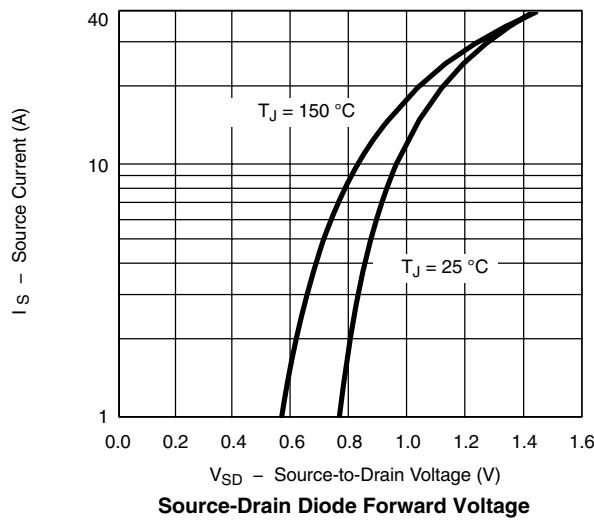
Capacitance



Gate Charge



On-Resistance vs. Junction Temperature

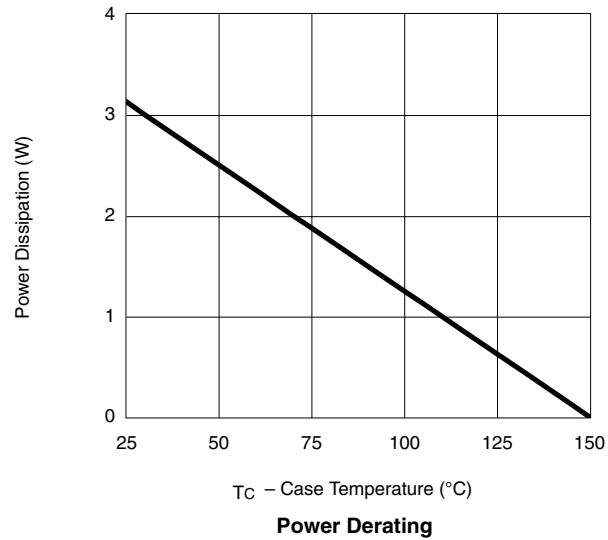
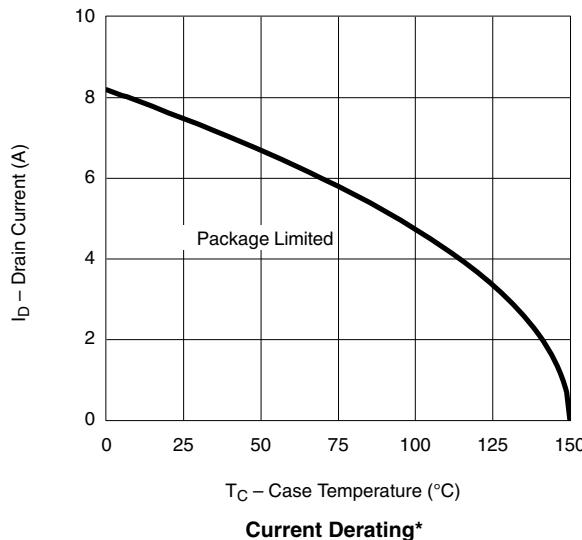
TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted


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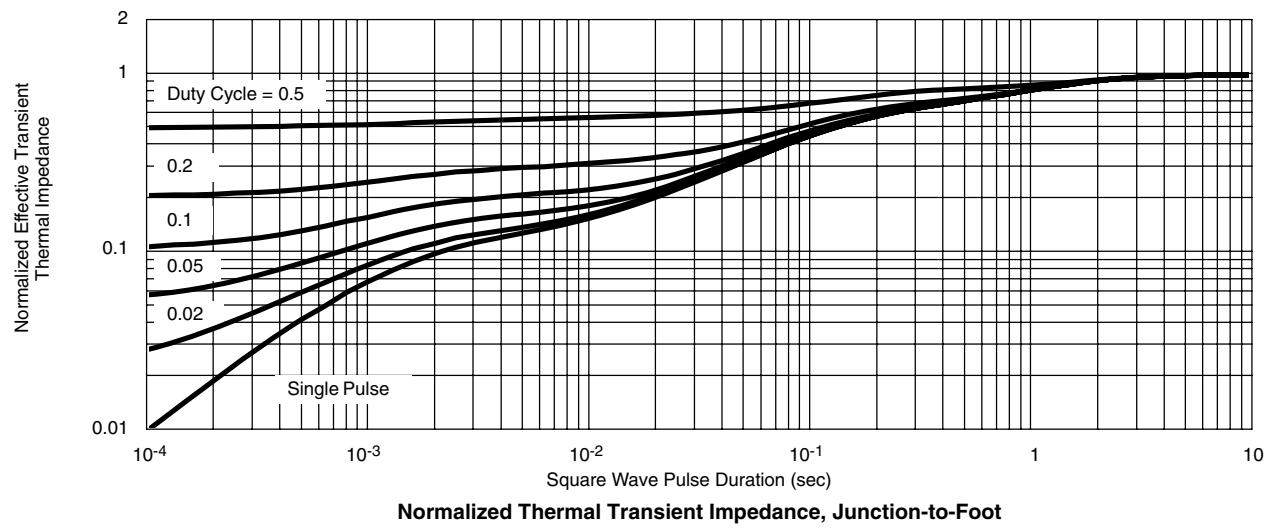
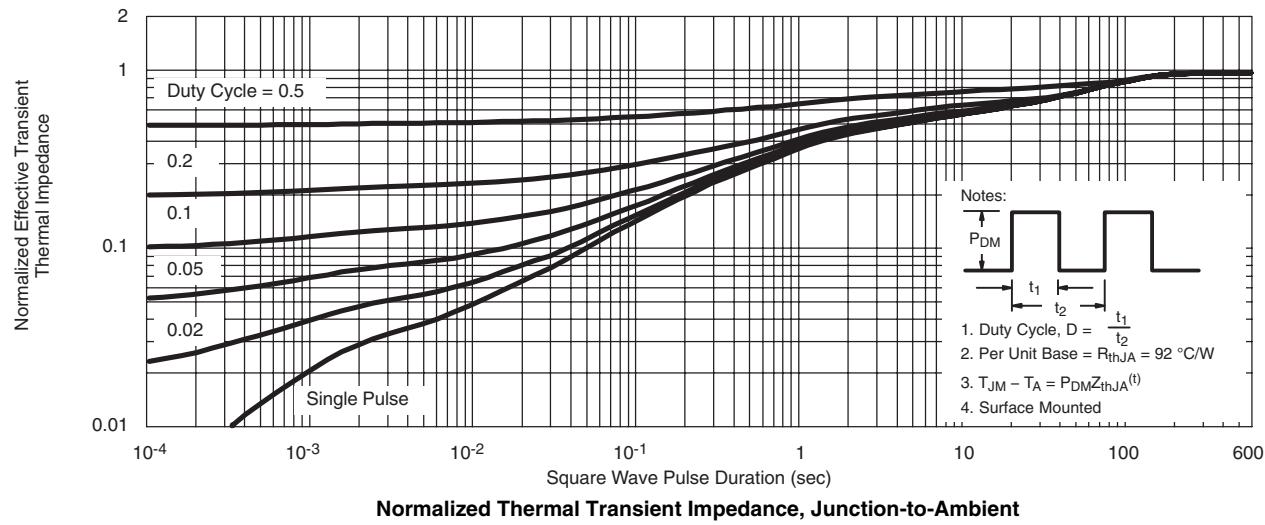
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*The power dissipation P_D is based on $T_{J(max)} = 150$ °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.

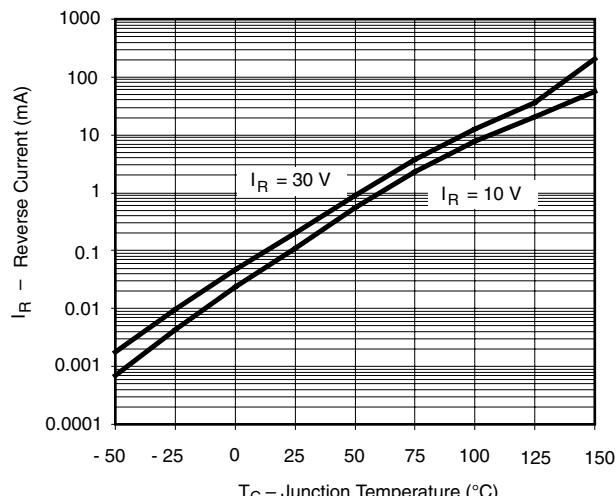
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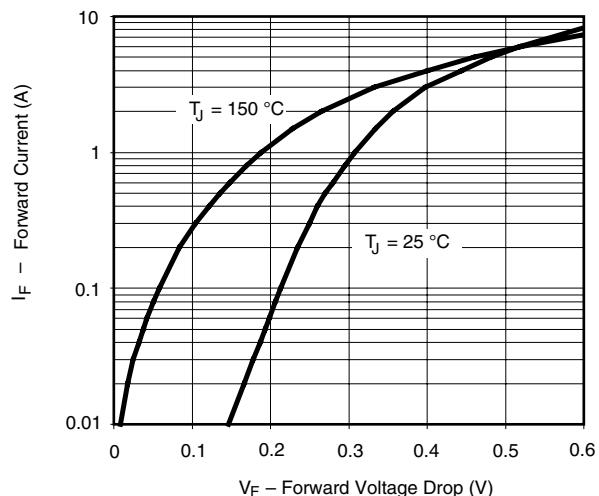
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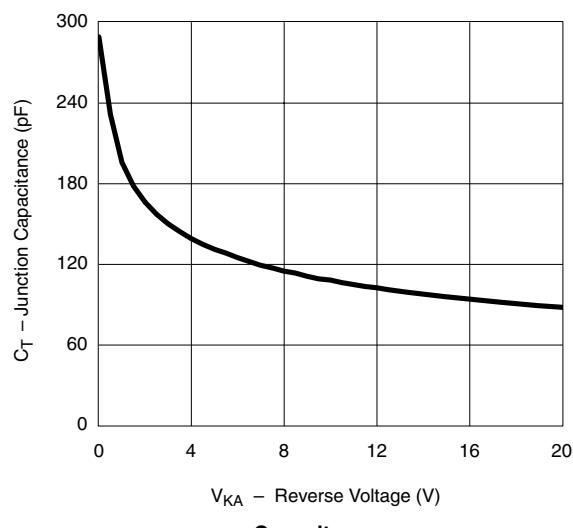
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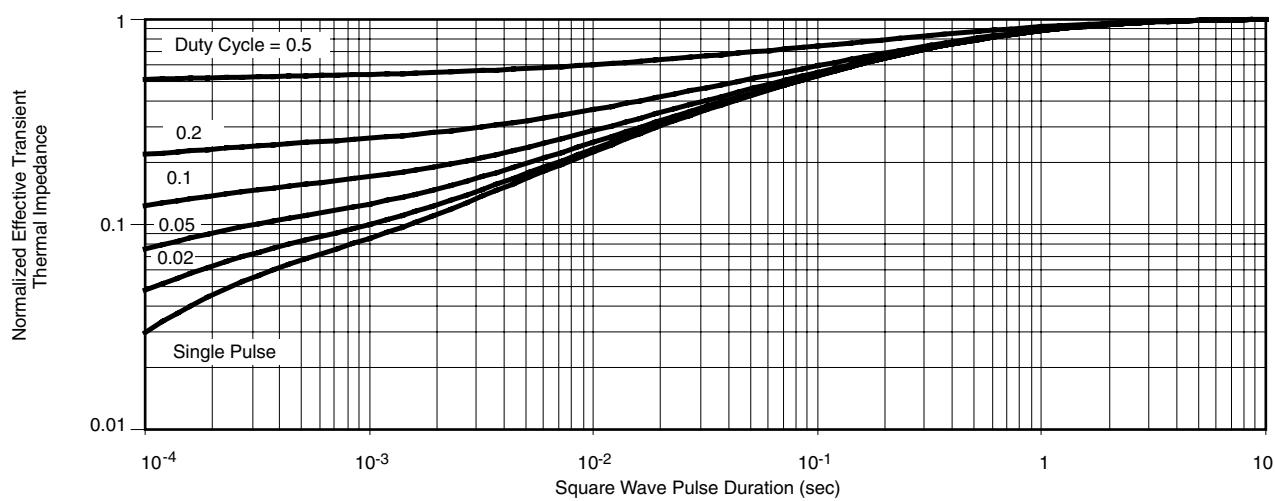
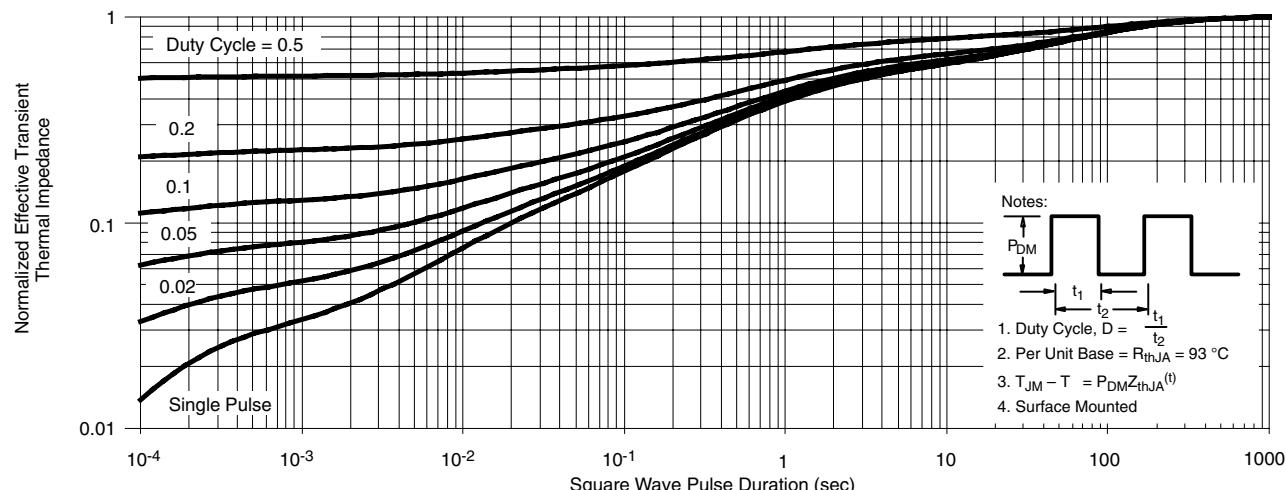
Reverse Current vs. Junction Temperature



Forward Voltage Drop



Capacitance

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